

Fig. 1 Prior art

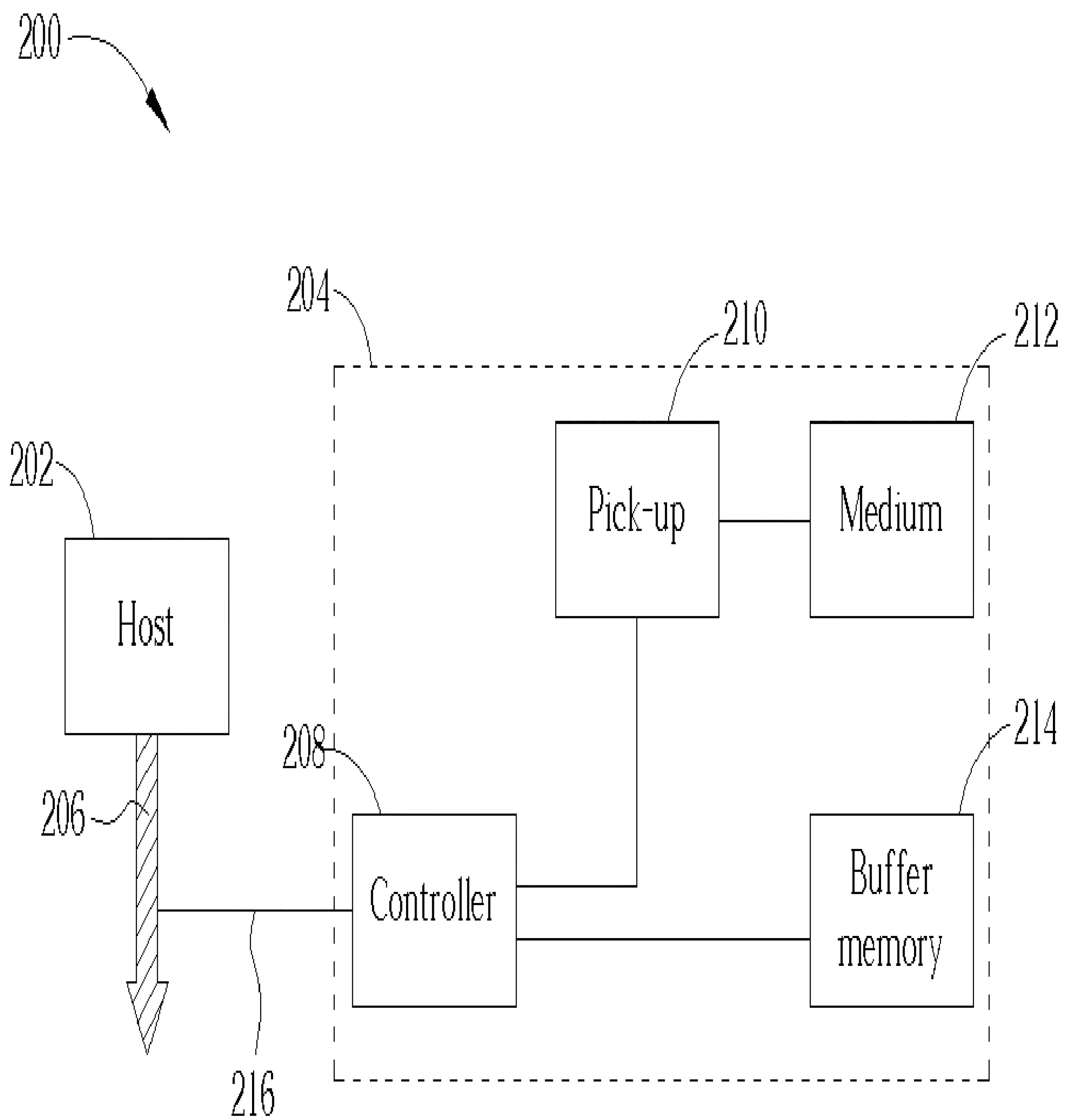


Fig. 2 Prior art

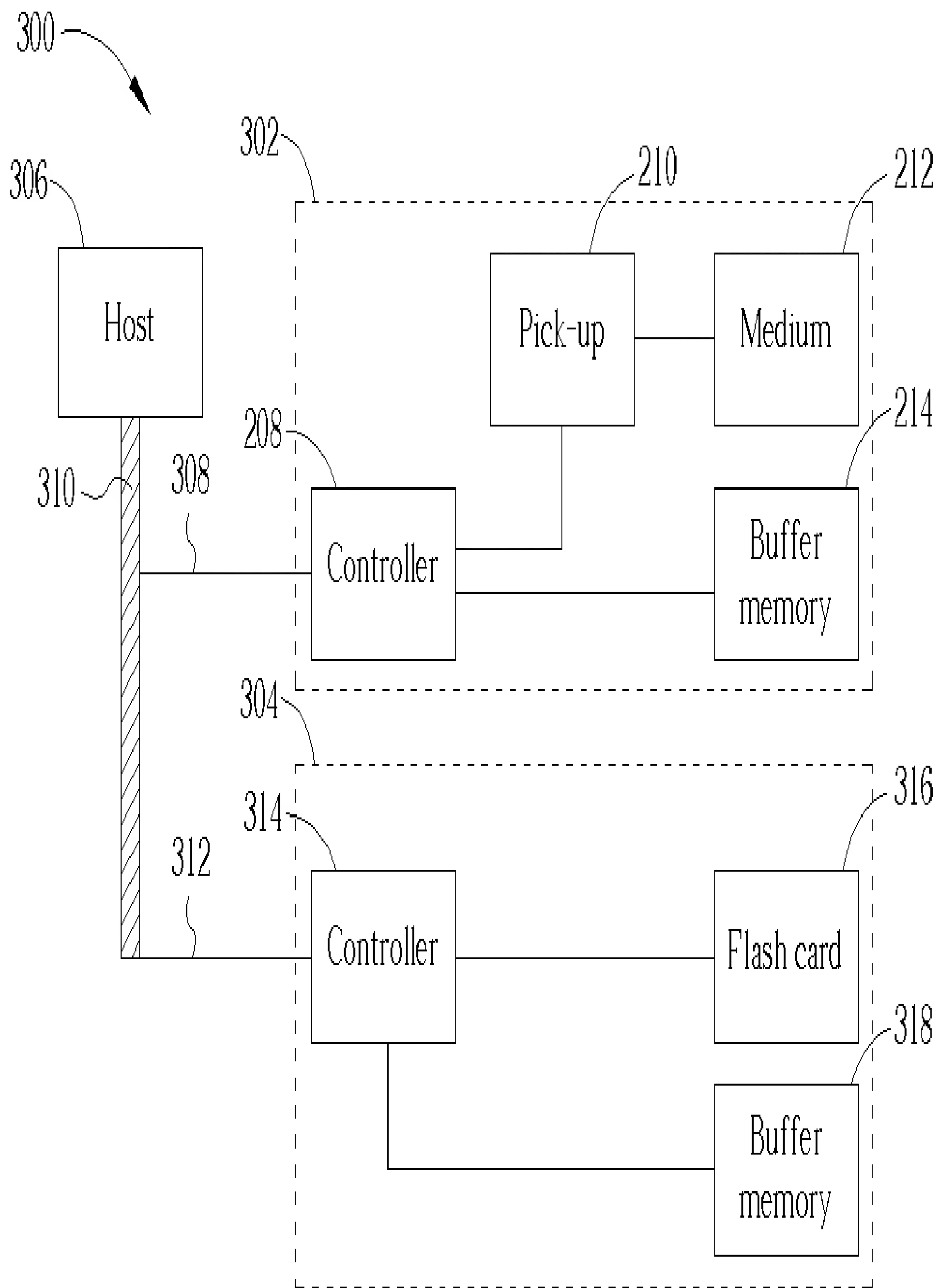


Fig. 3 Prior art

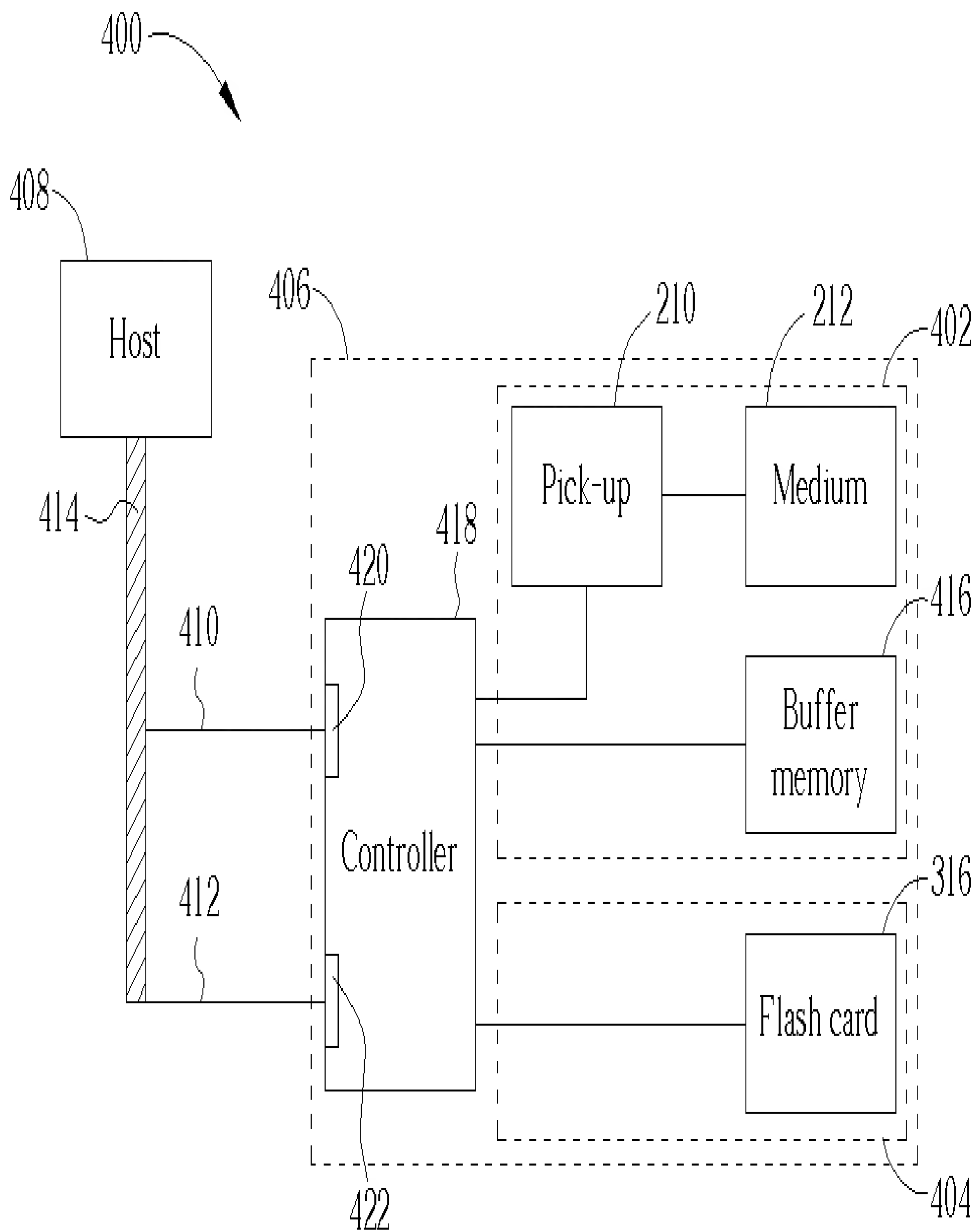


Fig. 4 Prior art

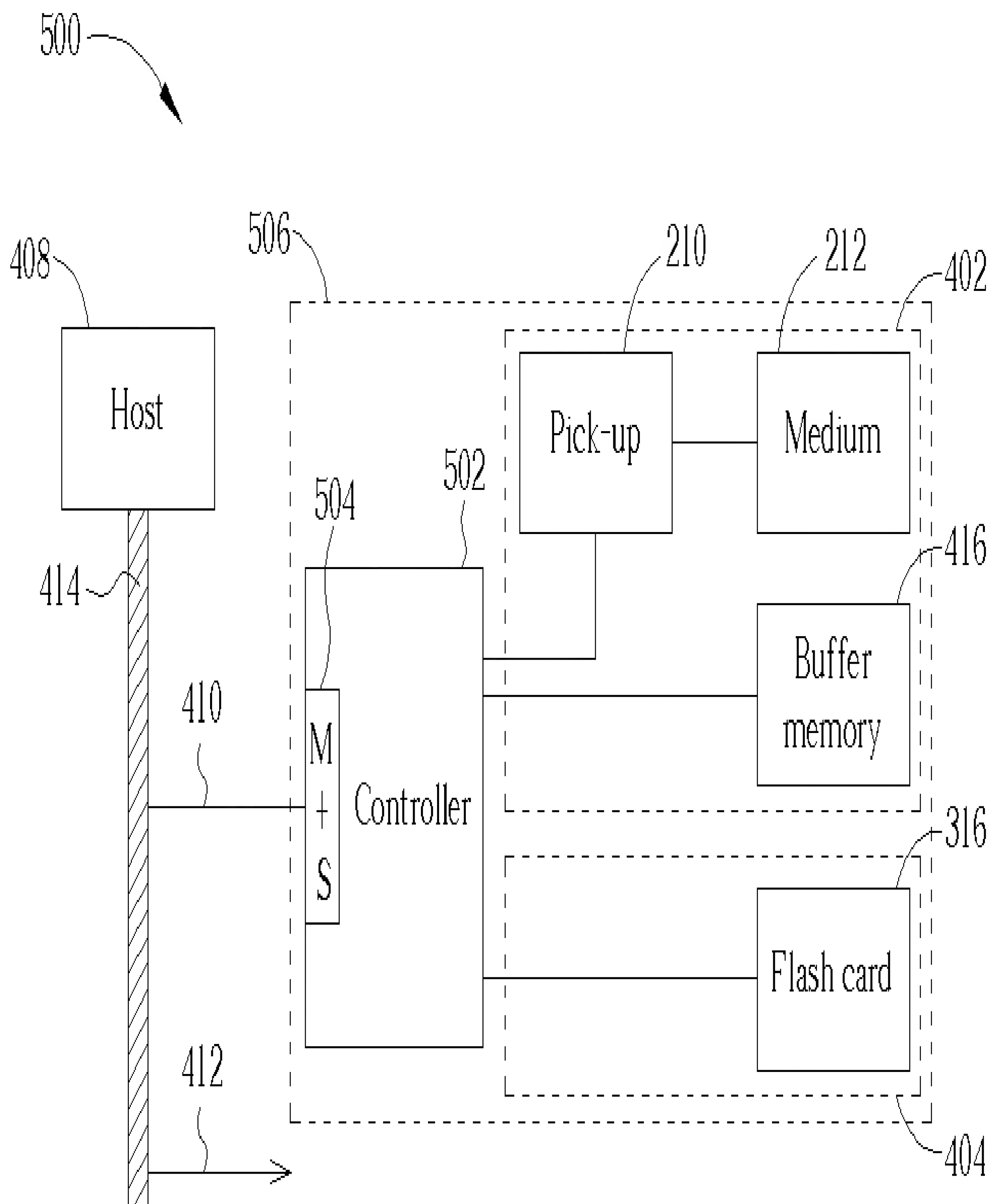


Fig. 5 Prior art

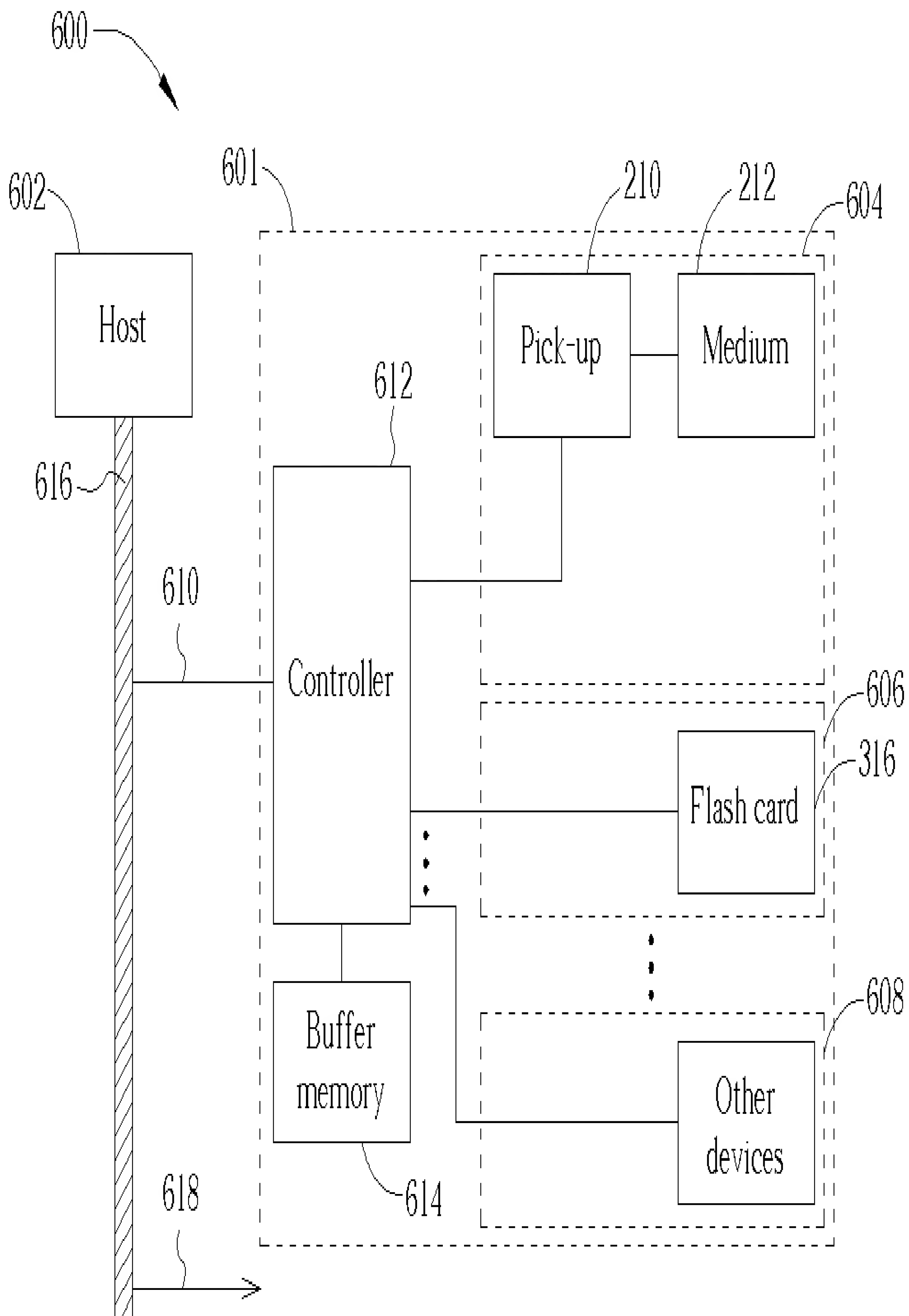


Fig. 6

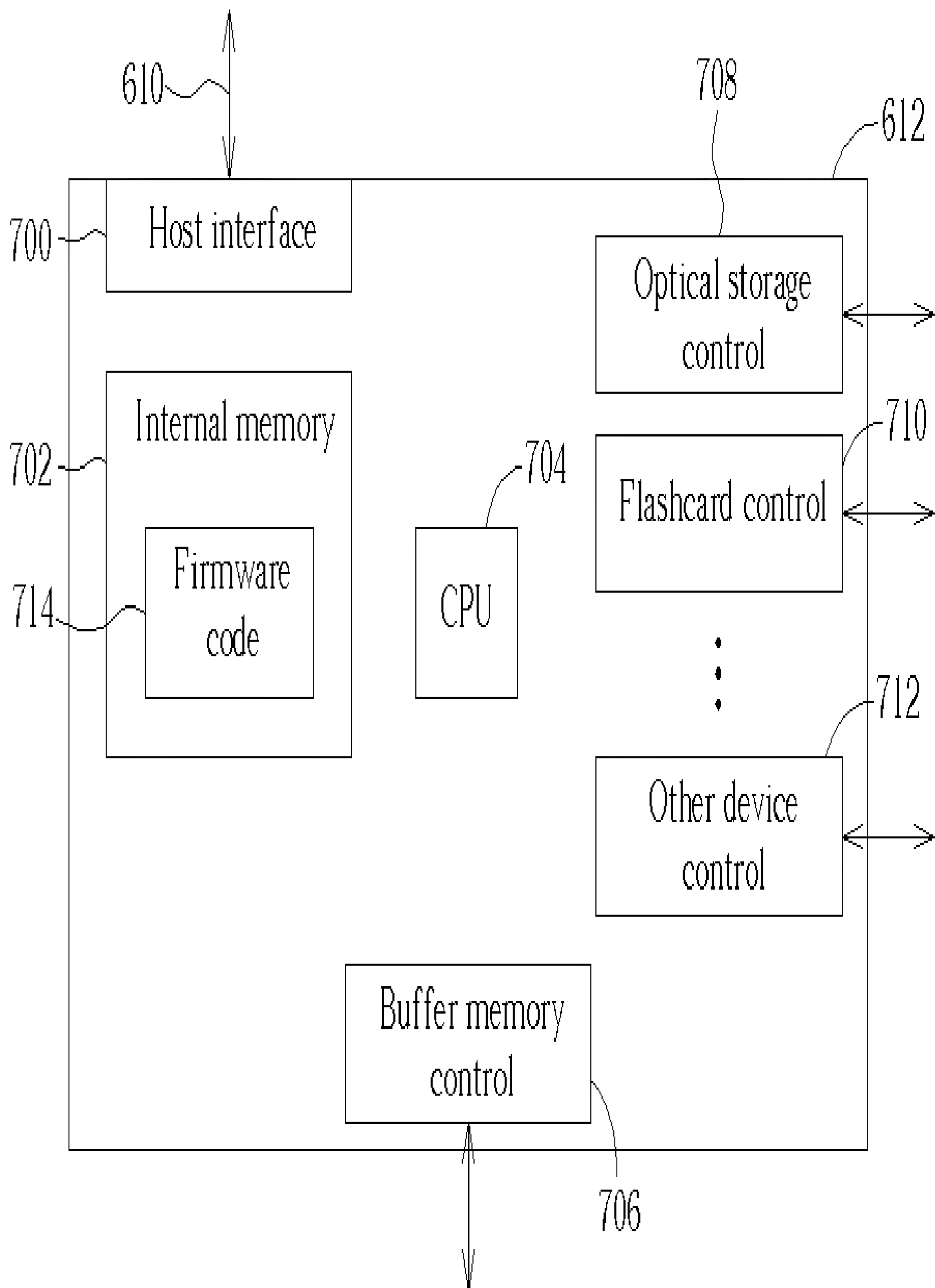


Fig. 7

Bit	7	6	5	4	3	2	1	0
Byte								
0	Operation code (28h)							
1	LUN (Obsolete)			DPO (0)	FUA	Reserved		RelAdr
2	(MSB)  Logical Block Address      (LSB)							
3								
4								
5								
6	Reserved							
7	(MSB)  Transfer Length      (LSB)							
8								
9	Vendor-Specific	Reserved			NACA	Flag	Link	
10	PAD							
11								

Fig. 8 Prior art



Register	7	6	5	4	3	2	1	0
Features	na							
Sector count	Sector count							
LBA Low	LBA (7:0)							
LBA Mid	LBA (15:8)							
LBA High	LBA (23:16)							
Device	obs	LBA	obs	DEV	LBA (27:24)			
Command	C0h							

Fig. 9 Prior art

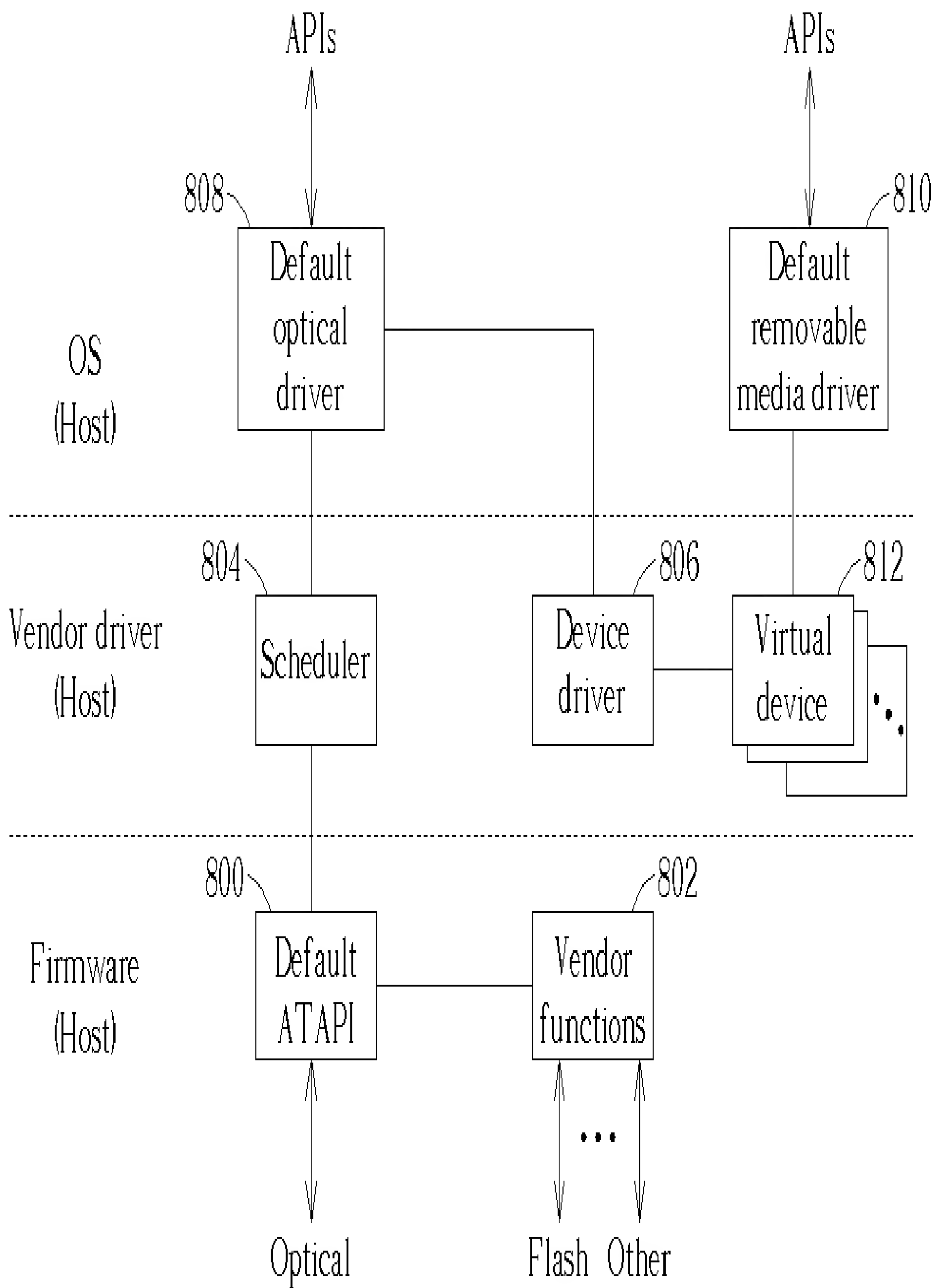


Fig. 10

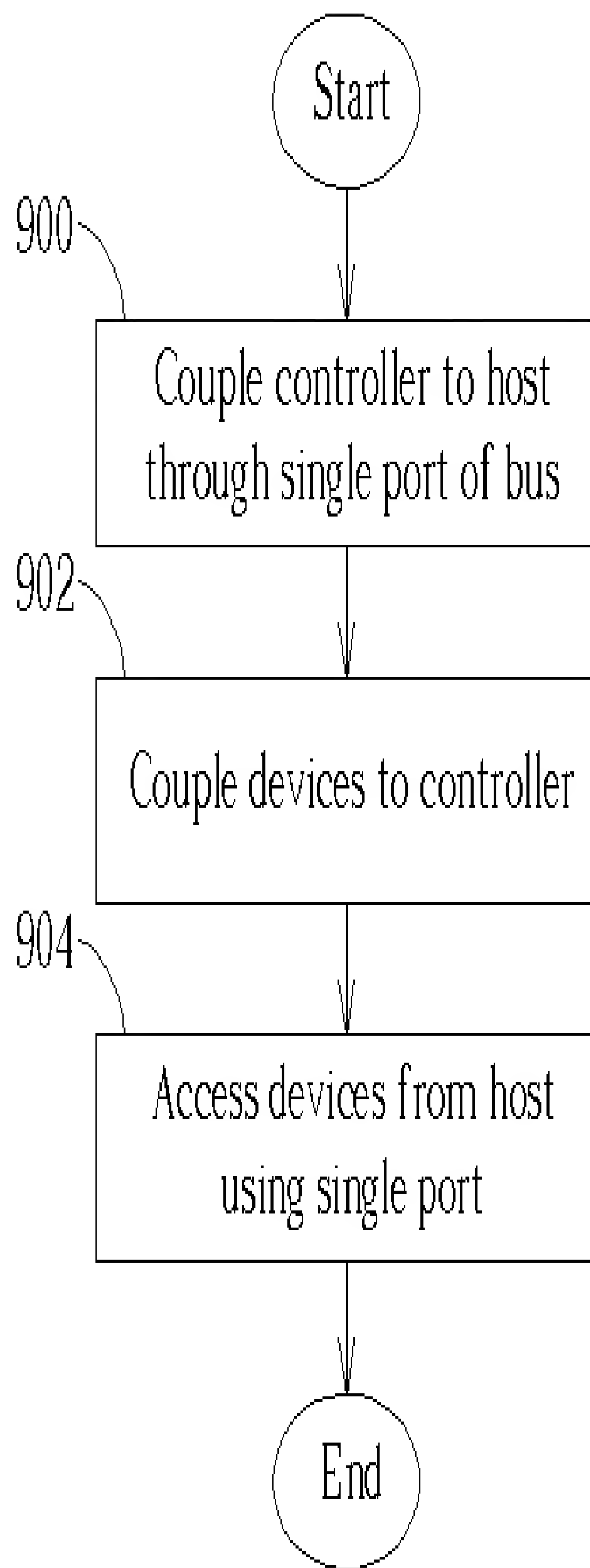


Fig. 11